

so, hiding the cryptographic algorithm that we are using and claim that we are secure is not valid. On the other side, the key should be secret, and the implementation of the cryptosystem should be strong to avoid information leakage leading to reveal this secret key. Recently, these algorithms and the crypto systems implementing them are being subject to many cyber attacks. One important category of these attacks is the Side Channel Attacks (SCA) [2]. The idea behind these attacks in order to reveal the secret key is based on investigating the leaked side channel information such as consumed energy and execution time in cryptosystems.

In this paper, we will present recent SCAs for cryptosystems of two of the main public key cryptography algorithms (ECC and RSA), and the most common private key encryption algorithm (AES). Moreover, the useful countermeasures against these attacks will be presented.

2. Side Channel Attacks (SCAs) on ECC, RSA, and AES

The implementations of symmetric and asymmetric encryption algorithms including ECC, RSA, AES, are exposed to side channel attacks (SCAs). The attackers try to know the secret key of the running cryptosystem from leaked side channel information during execution. Typical SCAs include timing, power consumption and electromagnetic radiation attacks. In the next subsections we will explore the timing attacks and the power analysis attacks with their countermeasures.

2.1. Timing Attacks and Countermeasures

The first SCA was introduced by Kocher [2] which was called Timing attack. It was based on exploiting the non-constant execution time using different input values to reveal the secret information. The required execution time may vary due to many reasons such as conditional branches in the algorithm and performance optimizations. The next step will be analyzing the time for each execution to derive the secret information. Generally speaking, there are two types of timing attacks on Elliptic Curve Cryptography (ECC), the classical attacks and the particular point attacks.

The main operation in the ECC is the Elliptic Curve Scalar Multiplication (ECSM) in which the point (x, y) is multiplied by a scalar integer

(k). Collecting the execution time of many ECSMs is the main idea behind using the classical type of timing attacks [2]. The same (k) with different points will be used and for each operation the execution time is recorded. The attacker saves the obtained timings separately in S1 and S2 and let's assume that T1 and T2 are the average timing of S1 and S2, and let (ϵ) to be the final reduction average time. Then if $T2 - T1 \approx \epsilon$, this means that the guess regarding the secret key is correct.

The other type is the particular point attack [3] which exploits the Montgomery multiplication operation when doubling a point $(x=2, y=y)$. There is higher probability that the Elliptic Curve Scalar Multiplication operation has bigger average time when random inputs are used. This means for the above case when $x=2$, the attacker will notice small execution time. The countermeasure to this attack is randomizing the points by using the random projective coordinates countermeasure.

For the RSA, the first timing attack was proposed based on prime fields [2]. It is noticed that many implementations of public key cryptosystems are subject to timing attacks if they use final reduction in Montgomery multiplication algorithm [2]. One straight forward solution to protect the cryptosystems against this timing attacks is to fix the execution time of main operations which can be done by adding other fake operations. For example, performing fake subtraction in Montgomery multiplication if the final reduction step is not needed, and the opposite can be done in case of modular subtraction if addition is not required. This countermeasure is also effective in any attack that exploits the final reduction step.

Table 1. Countermeasures against classical timing and particular point attacks

Attack	Classical timing [2]	Particular Point [3]
Random Coordinates	Yes	-
Constant time arithmetic	Yes	Yes
Random Curve isomorphism [7]	Yes	-
Splitting Euclidean, Additive, multiplicative	Yes	Yes
Point Blinding [8]	Yes	Yes
Group scalar randomization	Yes	Yes

Making the execution time constant helps in protecting cryptosystems against the timing attacks, but sometimes it is difficult to be implemented. Another approach is to use

randomization techniques such as adding random delays to make the measurements inaccurate. Table 1 summarizes the useful countermeasures against Classical timing attack [2], and Particular point attack [3] on ECC, RSA and AES.

2.2. Power Analysis Attacks and Countermeasures

The dynamic power consumption of the digital integrated circuits that is built using CMOS technology depends on the change of data. In other words, switching from logic 1 to logic 0 or vice versa consumes dynamic power. The attacker can extract the key in Power Analysis attack (PAA), and the Electromagnetic Attack (EMA) by resolving the power consumption, and electromagnetic patterns. For the EMA there two attack types: simple (SEMA) and double (DEMA) [9]. The magnetic attacks depend on the leakage of electromagnetic fields due to current flows where the attacker can measure the electromagnetic radiation of a smart card with an oscilloscope, a flat coil and a Faraday cage. The countermeasures against EMA attacks include confining the radiation by metal layers and canceling the radiation using dual logic. The power analysis attack exploits the relationship between the processed data inside a cryptographic device and the dynamic power consumption by that device in order to recover secret information. These attacks have become a major threat to smart cards, mobile phones, and RFID devices. This enforces the need for efficient and PAA-resistant cryptographic algorithms. Since 1996, many researchers proposed solutions to secure the ECC implementations against PAAs [8] [10-22].

In the following sections, we are presenting the main two power analysis attack techniques: Simple Power Analysis Attack (SPA), and Differential Power Analysis Attacks (DPA) on ECC, RSA, and AES their countermeasures.

3. Simple Power Analysis (SPA) Attack on ECC, RSA, and AES

The main idea behind the simple power analysis (SPA) is to get the secret key (d) by watching power consumption to obtain side-channel leakage information. The SPA can be applied to ECC, RSA and AES. In the following subsections we will present the SPA attacks and their countermeasures for each of these cryptographic algorithms.

3.1. SPA Attack on ECC and Countermeasures

The basic operation in ECC is the ECSM which can be performed using the double-and-add algorithm [3]. When scalar bit equals to one the point Add (PADD) operation will executed, while the point Double operation (PDBL) will be executed for all bits in of the scalar integer (k). If power consumption trace pattern of PADD and power consumption trace pattern of PDBL are different from each other, the PADD presence is revealed by the implementation's side-channel leakage and thus, the value of the scalar bits will be revealed to retrieve the secret key from a single side-channel trace.

The classical SPA [8] can be applied by using straight forward Elliptic Curve Scalar Multiplication (ECSM) algorithms such as the double-and-add method. At each iteration, a PADD is performed only if the current bit is One, and so, the bits of the scalar can be recovered using the ECSM trace and based on the power consumption difference between PADD and PDBL.

```

1:  input P, k
2:  Q[0] ← P
3:  for i from m-2 downto 0 do
   3.1: Q[0] ← 2Q[0]
   3.2: Q[1] ← Q[0]+P
   3.3: Q[0] ← Q[ki]
4:  end for
5:  output Q[0]

```

Figure 1. Algorithm 1: Double-and-Add-Always

Walter in [23] introduced SPA on unified formulae attack which targets the indistinguishable point operation formulae countermeasure where at the end of Montgomery multiplication, a simple power analysis is needed to decide if a final subtraction is needed. This attack was improved in [24] by using conditional subtraction (resp. addition) after finishing modular addition (resp. subtraction) and all bits of scalar can be recovered by only one trace.

Countermeasures: All the countermeasures used to protect against the SPA are based on rendering the power consumption traces that are caused by the data and operation computations during an ECSM independent from the secret key. One way to prevent the SPA is to make the group operations indistinguishable in order to process the multiplier bits "1" and "0" in

indistinguishable way by inserting extra point operations.

MONTGOMERY POWERING LADDER	
Input:	$X, N,$ $E = (e_{k-1}, \dots, e_1, e_0)_2$
Output:	$X^E \bmod N$
1 :	$R_0 := 1; R_1 := X;$
2 :	for $i = k - 1$ downto 0 do
3 :	if $e_i = 1$ then
4 :	$R_0 := R_0 \cdot R_1 \bmod N;$ — multiplication
5 :	$R_1 := R_1 \cdot R_1 \bmod N;$ — squaring
6 :	else [$e_i = 0$]
7 :	$R_1 := R_1 \cdot R_0 \bmod N;$ — multiplication
8 :	$R_0 := R_0 \cdot R_0 \bmod N;$ — squaring
9 :	end if
10 :	end for
11 :	return R_0

Figure 2. Algorithm 2: Montgomery power ladder

As an example, the Montgomery ladder [11] and the double-and-add-always algorithms [8], ensure that the sequence of operations appear as a PADD followed by a PDBL regularly as shown in figure 1. The storage requirements for Double-and-add-always algorithm [8] are two registers store the result of each iteration. On the side, the Montgomery ladder [11] avoids SPA using dummy instructions making the ECSM execution time independent from the secret scalar hamming weight. The authors in [14] proposed secure and efficient ECSM method (Algorithm 3 shown in Figure 2 below). In this algorithm, the scalar bits string is portioned into two halves, then the common substring is extracted based on proportional logic operation from the two parts. The computational cost is measured by: $(k/2)$ PADD + k PDBL as can be obtained from figure 2. Another countermeasure against SPA is to insert an extra field operation to the used unified formula of PADD and PDBL in order to make all the implementations analogous and with same duration [10] [12] [13] [15] [16] [17] during the ECSM. In [15], the author presented a single formula that can be used for both PADD and PDBL operations.

The Side-channel atomicity is also considered among the useful countermeasures against SPA. In Side channel atomicity the operations sequence is presented as series of indiscernible side-channel atomic blocks for SPA [19] [20] [21].

```

Inputs: B2=( ... .... )2, B1=( ... .... )2, P
Output: dP.
1: Q[0]=Q[1]=Q[2]=Q[3]=O;
2: For e=1 to k/2 do /* scan B1 and B2 from
   LSB to MSB */
3: Q[2 + ] = Q[2 + ] + P; /* ADD */
4: P = 2P; /*
   DBL */
5: Q[1] = Q[1] + Q[3]; Q[2] = Q[2] + Q[3];
6: For e=1 to k/2 do
7: Q[2] = 2Q[2]; /* DBL */
8: Q[1] = Q[2] + Q[1];
Return Q[1].
    
```

Figure 3. Algorithm 3: ECSM based propositional logic operations [14]

The idea presented in these articles is based in inserting extra field operations and then dividing every process for atomic blocks resulting many instructions blocks that are equivalent in the power trace shape and duration by SCA. Authors of [21] provided different field operations in their research: one multiplication, two additions and one negation. In [19] PADD formula is expressed in six atomic patterns and PDBL formula in four atomic patterns. In their research, left-to-right ESCM that uses fast PDBL and mixed affine-Jacobian addition is performed, while in [20] they used squaring instead of multiplication and minimized the use of field addition negation. Two atomic patterns are proposed in [26] in Jacobian coordinates context. In [22], the authors presented LOEDAR scheme (Algorithm 4 shown in figure 4 below), which is resistant to SPA by performing two PADDs and one PDBL, independent on the value of k .

3.2. SPA Attack on RSA and recent Countermeasures

There are three power analysis attacks on RSA presented in [27]: “Single-Exponent, Multiple-Data” (SEMD) attack, “Multiple-Exponent Single-Data” (MESD) attack, and “Zero-Exponent, Multiple-Data” (ZEMD) attack. The power consumption traces are averaged to reduce noise. Then, the power consumption with a known exponent is compared with the power consumption with the secret key exponent in order to reveal the secret exponent.

```

Input: An integer  $k \geq 0$ , and a point  $P = (x, y) \in E$ 
Output:  $Q = kP$ 

Part 1: ECISM and  $P_v$  Accumulation
 $P_1 \leftarrow P, P_2 \leftarrow 2P, P_v \leftarrow \mathcal{O}$ 
for  $i$  from  $l - 2$  to  $0$  do
  if  $k_i = 1$  then
     $P_v \leftarrow P_v + P_2$ 
     $P_1 \leftarrow P_1 + P_2$ 
     $P_2 \leftarrow 2P_2$ 
  else
     $P_v \leftarrow P_v + P_1$ 
     $P_2 \leftarrow P_2 + P_1$ 
     $P_1 \leftarrow 2P_1$ 
  end if
end for
Return ( $Q = P_1$ )

Part 2: Error Detection and Recovery (EDR)
Pause Part 1 at the end of an iteration
if  $P_v + P_2 = 2P_1$  then
  save them as a new checked state;
else
  retrieve the previous checked state;
end if
Resume Part 1
    
```

Figure 4. Algorithm 4: LOEDAR Scheme [22]

Moreover, statistical tools are used to analyze the power traces, as an example: the Big Mac attack presented in [28] against sliding window methods on RSA implementations with unknown inputs. It is mentioned that if same operand is used for two multiplications, then the power consumption will be almost similar for both multiplications [28].

Table 2. Countermeasures against classical SPA on ECC, RSA, and AES

	Classic SPA [8]	SPA on unified formulae [23]	Big Mac attack k [28]	Horizontal power analysis [30]
Regular ECISM [8]	Yes	Yes	-	-
Indistinguishable formulae [8] [11]	Yes	Yes	-	-
Side-channel atomicity [19] [20] [21]	Yes	Yes	-	-
Random Curve isomorphism [7]	-	-	-	Yes
Point Blinding [8]	-	-	-	Yes

Moreover, if different iterations consume similar amount of power, it might indicate that the same values are used for different iterations and the square-and-multiply method is applied to the attack [29]. A leakage model for horizontal power analysis is presented in [30] where the attacker tries to guess the intermediate values of a multiplication and use a

statistical tool to determine whether the guessed value is correct or not. Contrary to the Big Mac attack, leakage method needs necessarily the input to be known, and like other mentioned attacks, it has higher success rates when using longer manipulated integers.

It is worth to mention that the algorithms: “add-and-double-always”, the “square and multiply always”, and the Montgomery ladder exponentiation have high amenability to the safe error attack. Table 2 shows the countermeasures against Classical SPA [8], SPA on unified formulae [23], Big Mac attack [28], and Horizontal power analysis [30] on ECC, RSA and AES.

4. Differential Power Analysis Attack (DPA) on ECC

There are two main techniques in this attack: data collection and data processing that will be discussed in this section.

There are many advanced Differential Power Analysis attacks that can be applied on ECC such as: refined power analysis (RPA) [31], zero power analysis (ZPA) [32], and doubling attacks [33].

In RPA (also called Goubin-type DPA) [31] attack, finding a point with zero for one of its coordinates on the elliptic curve $E(K)$ is the main idea. In [32] RPA is extended to ZPA depending on the idea of having a zero in the point is not a must because the needed zero value can be taken by the auxiliary register, and so any point with zero in its coordinates will be noticed. In [33] they talked about the Doubling Attack (DA) that relies on two query inputs: P and $2P$. The DA attack uses the same PDBL operations to compute both $d(p)$ and $d(2p)$. In [34] the relative DA attack is presented that derives the key bit so that the relationship between adjacent each two key bits will be $d_i = d_{i-1}$ or $d_i \neq d_{i-1}$.

An extension to DPA [35] is the Classical Correlation Power analysis (CPA) [8], where a model of the power consumption is created for use in the analysis phase of an attack. This attack tries to retrieve secret data and then searches for the power consumption traces where retrieved data is used by making guesses about the manipulated values and assumptions about the secret data. The difference between DPA and CPA is that DPA uses the difference of means while CPA involves Pearson’s correlation coefficient and compares real traces to each other.

Another attacks that uses two rounds techniques is the template attack [36] that involve two phases: building phase and matching phase. For the needed signal source, the attacker builds a precise model and this happens in the template building phases, while the matching phase comprises the actual attack. In [37], the author presented the attacks that don’t need assumptions from the attacker side which are called

online template attacks (OTA). Authors of [38] provided a case of template attack on Elliptic Curve Digital Signature Algorithm (ECDSA). They recovered many bits of many signatures by mounting template attack on ECDSA. In similar context, the lattice attack presented in [39] can be used to reveal bits of static private key of the signature.

The Carry-based Attack (CBA) [40] attacks the countermeasures of the ECSM depending on the power consumed because of carrying the propagation that happens after performing long-integer additions. Because of this process, each word's most significant bit of the scalar is detected. Furthermore Principal Component Analysis (PCA) [41] and more advanced techniques are used for performing the transformation of PCA on switched PADD and PDBL in order to identify the key bit. The authors in [42] presented the Address-bit attack in which the manipulation of address is exploited in almost all the ECSMs relying only on some bits of a scalar. The main idea in this attack is to know if the manipulated addresses are the same for different iterations by performing collision analysis.

In [43], the authors presented Same Values Power Analysis (SVA). Performing the same operations and using the same value on some specific points during a PDBL or a PADD is the main idea behind using SVA. On the other hand, regardless of the point being randomized or not, manipulation have to be performed over the same values. To find out if a specific point will appear or not, many traces are needed and to disturb this attack, scalar randomization technique [44] is the most appropriate. In [45] SVA was extended to horizontal SVA taking the advantage of repeated values during point doubling and aims at some scalar bits per ECSM. In doubling, the attacker performs two squares to the same input and to make sure that the squared values are the same and is happening on the current bit, the attacker analyses each square. SVA can be used against unified formulae to identify the elliptic curve operation and distinguish doubling operation from addition. Then SVA beats off all points formulae countermeasure that are indistinguishable. By this attack, only one trace is sufficient to discover all scalar bits because it can be performed independently at each iteration.

Countermeasures: There are many techniques used to resist DPA attacks [7] [8] [31] [44] [46] [47]. Randomizing the intermediate data is the straightforward approach that makes it possible to render hypothetical leakage values calculation. In [8] many other countermeasures proposed against DPA attack. The first approach is to build the scalar by adding a multiple of $\#E$. For any random number r and $k' = k + r\#E$, we have $k'P = kP$ since $(r\#E)P = O$. The second approach is to make RPA/ZPA more difficult by blinding the point P . In this approach, kP is $k(P+R)$ and at the end of the computation S that is

kR is subtracted. In [46] a modification on the blinding technique that were proposed in [8] is proposed in order to defend DS. Also, and in several studies, left-to-right multiplicand processing such as in Montgomery ladder cannot defend many attacks [33]. The Montgomery ladder [11] was attacked by the relative DA proposed in [34]. For the projective coordinates, randomizing (X,Y,Z) with a random $\lambda \neq 0$ to $(\lambda X, \lambda Y, \lambda Z)$ makes templates collection more difficult and randomization technique will not resist the RPA since zero cannot be randomized effectively [48].

There are other randomization methods suggested by Ciet and Joye in [44]. They proposed the Euclidean splitting to resist DPA attacks. In which $Q = [k_1]P + [k_2]S$ is computed with $S = [r]P$, $k_1 = k \bmod r$, $k_2 = [k/r]$, where r is a random integer so that $r = k/2$. When randomization method is used jointly with Blinding the point P technique, disturbing RPA/ZPA become more effortless and easier [31][49][50]. The work presented in [7] is about applying ECSM for isomorphic curve for changing each complete ECSM execution's median representation. Researchers in [47] showed an ECSM algorithm that resists PAS by ensuring a computation behavior that is uniform and secure and can resist PAA attacks. In [52], an improved ECSM algorithm called the window method is presented. In window method, when the width of the window is w , up to $(2w-1)P$ multiplies of the point P are computed in advance and stored. And since scalar k can process w bits at a time, k is recoded to the radix 2^w . k can be recorded in a way so that the average density of the nonzero digits in the recoding is $1/(w+\epsilon)$, where $0 \leq \epsilon \leq 2$ depends on the algorithm.

The authors in [22] presented an extended method for LOEDAR to thwart power analysis attacks by randomizing initial point based on the ideas presented in [53]. Algorithm 4 (shown in Figure 3) describes how a random point R blinds the initial points P_1 ; P_2 and P_v . The extended LOEDAR scheme can thwart DPA/ RPA/ZPA, where R randomizes zero-values registers that are used by RPA and ZPA. In [54], the authors presented the Additive splitting countermeasure were $[k]P$ is replaced by a random r to compute $Q = [k-r]P + [r]P$. Another technique is presented for the same purpose that is consequently computing $[k-r]P$ and $[r]P$. The Multiplicative splitting presented in [55] suggests computing $Q = [k']S$, with $S = [r]P$, $k = kr-1 \bmod \#E$. Selecting a small size random r (16 bits for instant) will give a good trade-off between performance and security.

A new efficient countermeasure to overcome first-order CPA attacks was proposed in [56] exploiting the dependency between a function of the secret parameter and the mean of an instantaneous leakage. This countermeasure avoids the need for scalar blinding and assumes randomizing the

Montgomery representation of the internal results and the field operations are conducted in Montgomery domain. But, this countermeasure was broken by first-order CPA due to the weakness caused by the binomial values of λ [57]. The attack in [57] observes the device power consumption or the electromagnetic emanation for several executions of the ECSM parameterized by different public inputs P with the same secret scalar k. The *i*th observation is algorithmically related to the *i*th input point P_{*i*} and the secret bit of k.

In [58], the authors presented a secure ECSM method against SPA, DPA and timing analysis. It scans the scalar from left-to-right with a window method, and the secret key is read in digits from the MSB to the LSB to save additions. But, before starting the scalar multiplication, a pre-computed table has to be filled with all dP points for each possible digit d. On the other hand, selecting each window size randomly can improve the security against statistical attacks (DPA) in the window method. But, using the SPA attack to count the PDBLs between the PADDs can help in recovering the window size, thus, dummy PADDs are inserted during kP computing to hide window sizes to break the synchronization of DPA traces.

Table 3. Summary of the countermeasure against known DPA attacks on ECC, RSA, and AES

	Temple Attacks- ECC DA [31]	OTVA [31]	CHA [40]	SVA [43]	Horizontal SVA- AS1	SRND [27]	MRSD [27]	Address-bit DPA- L201	Comp Power Analysis ZEMD [27]	Heterogeneous DPA- FTL [80]	RPA [31]	ZPA [32]	CPA [8]	MRSD [66]
Random Coordinates [8]	Y	-	-	-	-	-	-	-	-	-	-	-	Y	-
Random Curve isomorphism [44]	Y	Y	-	-	-	-	-	-	-	-	-	-	Y	-
Point Blinding [8]	-	Y	-	-	Y	-	-	-	-	-	-	-	Y	Y
Group scalar randomization [8]	-	-	-	X	Y	I	-	I	-	-	-	-	I	I
Additive splitting [54]	Y	-	-	X	Y	I	-	Y	-	-	-	-	I	I
Euclidean splitting [44]	Y	-	-	-	Y	I	-	Y	-	-	-	-	I	I
Multiplicative splitting [53]	-	-	-	-	Y	I	-	Y	-	-	-	-	I	I
Window M-ary method [52]	Y	-	-	-	Y	I	-	Y	-	-	-	-	I	I
Random Initial Point [22] [53] [71] [72]	I	I	-	-	Y	I	-	Y	-	-	-	-	Y	Y
Random/Montgomery Representation of internal results [56]	Y	I	-	-	Y	I	-	Y	-	-	-	-	Y	Y
Message Masking [27]	-	-	-	-	-	Y	Y	-	Y	Y	-	-	-	-
Random Window-scanning [75]	Y	Y	-	-	Y	I	-	Y	I	Y	Y	-	Y	Y
Random masking [77]	Y	Y	-	-	Y	-	Y	Y	I	Y	Y	X	Y	Y
Hardware balancing [78]	Y	Y	-	-	Y	-	Y	Y	I	Y	Y	X	Y	Y
Algorithmic balancing [79]	Y	Y	-	-	Y	-	Y	Y	I	Y	Y	X	Y	Y

The same number of PDBLs and PADDs are computed in each scalar multiplication to secure against timing analysis by using dummy operations. In [59] a dual field ECC processor was proposed that resists several Power Analysis Attacks such as the doubling attack, DPA and SPA. This Dual field ECC processor uses the heterogeneous dual-processing element (dual-PE) architecture. This architecture uses a priority-oriented scheduling of right-to-left double-and-add-always ECSM with randomized processing technique and masked intermediate data

technique. The authors in [60]- [64] proposed different approaches to secure ECC implementations in GF(p) and GF(2m) against SPA and DPA attacks.

Countermeasure of ECC power attacks over GF(p) are proposed in [65]. Many algorithms were designed to protect against SPA such as Montgomery ladder scalar multiplication. In this algorithm, base point building technique is used for the key upper half protection in order to minimize the time overhead for the purpose of DPA and ZPA impeding, while key splitting scheme is used to protect the lower half. In this scheme, a private key is split into two equal parts the key size (m) is even, and into two unequal parts if m is odd.

5. Discussion and Conclusions

Based on the presented attacks in this research and their countermeasures, we can say that attacks that separately try to disturb a cryptosystem implementation usually cannot be achieved and the implementation can be protected. On the other hand, jointly attacks on an implementation can succeed in disturbing the cryptosystem implementation. As an example, the combined attack proposed in [66] that is designed to disturb additive scalar splitting. Computing [k]P is displaced by computing [k - r]P + [r]P, where r is a random value, because this combined attack aims to disturb random additive splitting countermeasure and it is necessary to know that k-r and r are correlated to k and thus, using a statistical analysis different computations can reveal the secret key k. For the purpose of statistical repartition recovery, two combined attacks are proposed in literature the first one is second order safe-errors attack that is a combination of two C safe-errors or two M safe-errors. The second one is a combination between safe-error and an ADPA.

The author in [67], proposed another combined attack. The author’s aim was to disturb the atomicity countermeasure by combining fault attack and SPA. At the beginning of an operation like double-and-add, one of the coordinates of the base point P have to be set to zero. A multiplication that has zero for one of its operands can easily be identified by tracing, and the attacker can differentiate between PDBL and PADD. The idea in this attack is to allow the recovery of all scalar bits by only one trace. The extension of this attack to ECSM is possible when PADD can use the points under specific conditions which is the scalar must have zero points. Table 4 below shows combined attacks and proposed countermeasures.

Furthermore, in [68] a combination between SPA and invalid point attack was proposed. In ECSM the fault is firstly introduced to the base point and let P’ is the faulty point positioned on another curve. And because of the low order of this point -ord(P’)-, it will appear while computing k[P] resulting in

incorrect elliptic curve points. On the other side, there are efficient architectures and implementations for cryptographic functions including elliptic curves on configurable hardware [69]. Also, the reconfigurable FPGA implementations allow the users to optimize the design according to security and efficiency tradeoffs especially when using for different applications [70] with different requirements such as wireless sensor networks [71].

Table 4. Combined attacks and proposed countermeasures

	C safe-error and ADPA	M safe-error and ADPA	Zero word and SPA	Point of low order and SPA
Regular ECSM [8]	X	X	X	
Side-channel atomicity [19] [20] [21]	X	X	X	
Additive splitting [54]	X	X		
Euclidean splitting [44]	X	X		
Random register address [94]		Yes		
Input point validity [88]				Yes

In conclusion, this paper represents a comprehensive study of recent and major Side Channel Attacks (SCAs) on the two of most common asymmetric key cryptography algorithms, namely, ECC and RSA, and on AES which is the most secure symmetric encryption algorithm used today. This paper concludes that there is no only one countermeasure that is ready for covering all of the existing attacks and that's why countermeasures need to be combined and grouped in order to defeat all the attacks. On the other side, and due to the non-negligible cost of each countermeasure, it is impossible to let all the countermeasures work at the same time. Therefore, it is necessary to be accurate and aware when choosing a specific countermeasure and consider the main security-performance tradeoff.

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